

Remarks

Applicants and their Attorneys thank the Examiner for the courtesy extended in the Examiner Interview of January 17, 20001. Reconsideration of this Application is respectfully requested.

Upon entry of the foregoing amendment, claims 12-30 are pending in the application, with claims 12, 18 and 24 being the independent claims. These changes are believed to introduce no new matter, and their entry is respectfully requested.

Based on the above amendment and the following remarks, Applicant(s) respectfully request(s) that the Examiner reconsider all outstanding objections and rejections and that they be withdrawn.

Rejections under 35 U.S.C. § 102

In the Advisory Action mailed February 7, 2001, the Examiner maintained the rejections of independent claims 12, 18 and 24 and objections to dependent claims 13-17, 19-23, and 25-30. During the Examiner Interview of January 17, 2001, the Examiner agreed that the only rejections being maintained were those under 35 U.S.C. § 102(b) based on Keller, "Look-ahead Processors."

Based on discussions with the Examiner, Applicants have amended claim 12 to distinguish over the prior art of record. These changes do not constitute new matter and their entry is respectfully requested. Also based on these discussions, Applicants maintain their position as to the patentability of independent claims 18 and 24 over the prior art of record.

Conclusion

All of the stated grounds of objection and rejection have been properly traversed, accommodated, or rendered moot. Applicants therefore respectfully request that the Examiner reconsider all presently outstanding objections and rejections and that they be

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withdrawn. Applicants believe that a full and complete reply has been made to the outstanding Office Action and, as such, the present application is in condition for allowance. If the Examiner believes, for any reason, that personal communication will expedite prosecution of this application, the Examiner is invited to telephone the undersigned at the number provided.

Prompt and favorable consideration of this Amendment and Reply is respectfully requested.

Respectfully submitted,

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Version with markings to show changes made

The following claim has been amended:

12. (Once amended) A system for register renaming in a computer system capable of out-of-order instruction execution, comprising:

a temporary buffer comprising a plurality of storage locations for storing execution results, wherein an execution result for an instruction in an instruction window is stored at one of said plurality of storage locations, said one of said plurality of storage locations being assigned to said instruction in said instruction window; and

tag assignment logic for receiving data dependency results from a data dependency checker and for outputting a tag comprising a[n operand] temporary buffer storage location address in place of a register address for an operand of a first instruction, wherein said [operand] temporary buffer storage location address is an address of said operand in one of said plurality of storage locations if said first instruction is dependent on a previous one of said plurality of instructions in said instruction window for said operand.

